

## Patent Assignment Abstract of Title

**Total Assignments: 1**

|                                       |                              |                       |
|---------------------------------------|------------------------------|-----------------------|
| <b>Application #:</b> <u>10730852</u> | <b>Filing Dt:</b> 12/09/2003 | <b>Patent #:</b> NONE |
| <b>PCT #:</b> NONE                    |                              | <b>Issue Dt:</b>      |

**Inventors:** Kiyonori Oyu, Atsushi Ogishima

**Title:** Semiconductor memory device with less threshold variation

**Assignment: 1**

|                                       |                                |                                |                              |
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**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

|  |                            |
|--|----------------------------|
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